## WE CLAIM:

1. A driver circuit for driving signal lines of a matrix type display device, comprising:

pulsewidth modulation circuitry for generating pulsewidth modulated video data;

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driver circuitry for driving said signal lines in accordance with the pulsewidth

6 modulated video data.

- 2. The driver circuit according to claim 1, wherein said driver circuitry
- 2 comprises level-shifting circuits.
- The driver circuitry according to claim 1, wherein said pulsewidth
- 2 modulation circuitry comprises a programmable logic array.
- 1 4. The driver circuitry according to claim 1, wherein said pulsewidth
- 2 modulation circuitry comprises an application specific integrated circuit.
- The driver circuit according to claim 1, wherein said signal lines are
- 2 connected to emitter elements of a field emission display.
- 1 6. The driver circuit according to claim 1, wherein said pulsewidth
- 2 modulation circuitry generates the pulsewidth modulated video data based on RGB video
- 3 \_\_data supplied thereto.

-16-

2

1	7.	The driver circuit according to claim 1, wherein said driver circuitry is
2	provided on a	chip other than a chip on which said pulsewidth modulation circuitry is
3	provided.	
1	8.	The driver circuit according to claim 1, wherein said driver circuitry
2	comprises driv	ver circuits that are loaded in parallel with the pulsewidth modulated video
3	data.	
1	9.	A matrix type display device comprising:
2	display	y elements connected to row lines and column lines; and
3	a drive	er circuit for driving said column lines, said driver circuit comprising:
4		pulsewidth modulation circuitry for generating pulsewidth modulated
5	video data; an	d
6		driver circuitry for driving said column lines in accordance with the
7	pulsewidth m	odulated video data.
1	10.	The matrix type display device according to claim 9, wherein said driver
2	circuitry con	prises level-shifting circuitry. –
1	. 11.	The matrix type display device according to claim 9, wherein said display
2	device is a fie	ld emission display device.
1	12.	The matrix type display device according to claim 9, wherein said display

device is a plasma display device.

1	13. The matrix type display device according to claim 9, wherein said	
2	pulsewidth modulation circuitry comprises a programmable logic array.	
1	14. The matrix type display device according to claim 9, wherein said	
2	pulsewidth modulation circuitry comprises an application specific integrated circuit.	
1	15. The matrix type display device according to claim 9, wherein said	
2	pulsewidth modulation circuitry generates the pulsewidth modulated video data based or	
3	RGB video data supplied thereto.	
1	16. The matrix type display device according to claim 9, wherein said driver	
2	circuitry is provided on a chip other than a chip on which said pulsewidth modulation	
3	circuitry is provided.	
1	17. The matrix type display device according to claim 9, wherein said driver	
2	circuitry comprises driver circuits that are loaded in parallel with the pulsewidth	
3 —	modulated video data.	
ナ	18. A method of driving signal lines of a matrix type display device,	
$\sqrt{2}$	comprising:	
3	generating pulsewidth modulated video data; and	
4	driving said signal lines in accordance with the pulse-width modulated data.	
1	19. The method according to claim 18, wherein said matrix type display	
2	device is a field emission display device.	

- 20. The method according to claim 18, wherein said matrix type display
- 2 device is a plasma display device.

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- 21. The method according to claim 18, wherein the pulsewidth modulated
- video data is generated based on RGB video data.

-19-